

Preliminary Specifications

Final Specifications

Module	LCD Driver Board
Model Name	DB-LD0A-01
Document Version	Rev.V0

Customer	

Approved by	Date
_____	_____
Notice: This Specification is subject to change without notice.	

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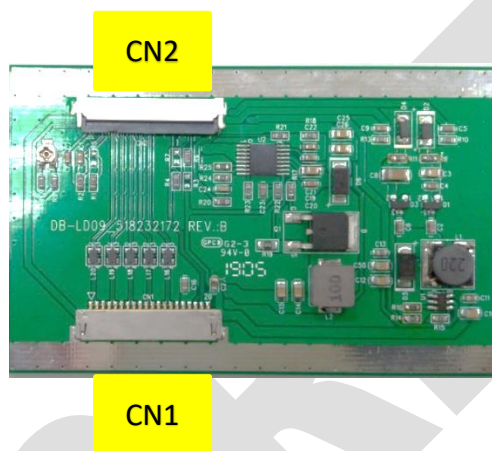
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1. Application

- VGH Positive Charge Pump Control
- VGL Negative Charge Pump Control
- AVDD Charge Pump Control
- VCOM Control
- Led Driver Over Current Protection
- Led Driver Over Voltage Protection
- Led Driver Adjustable PWM Dimming



2. Applicable Panel Type

Innolux NJ0801A-10D

3. Electrical Characteristics

Parameter		Min.	Typ.	Max.	Unit
Input Voltage	VDD	3.0	3.3	3.6	V
	VCC	---	5	5.5	V
Efficiency	Eff.	---	80	---	%
Output Current	IDD	6	24	40	mA
	IGH	0.07	0.3	1	mA
	IGL	0.08	0.31	1	mA
	IAVDD	10.5	42	70	mA
Output Voltage	VGH	23.5	24	24.5	V
	VGL	-6.1	-5.6	-5.1	V
	AVDD	12.9	13	13.1	V
	VCOM	---	---	---	V
Dimming	PWM Level	2.5	---	5.0	V
	Duty Ratio	0	---	100	%
	Frequency	0.2	---	20	KHz
LED Driving	VLED	8.4	9.3	10.2	V
	ILED	---	540	585	mA

4. Interface

4.1 Input Connector :

Location CN1 – 20 pin wafer, DF19 or equiv.

Pin Assign and Definition

Pin No.	Symbol	Pin No.	Symbol	Pin No.	Symbol	Pin No.	Symbol
1	LVDS0-	6	GND	11	LVDSCLK+	16	VCC
2	LVDS0+	7	LVDS2-	12	GND	17	VCC
3	GND	8	LVDS2+	13	LVDS3-	18	SELB
4	LVDS1-	9	GND	14	LVDS3+	19	Dimming
5	LVDS1+	10	LVDSCLK-	15	GND	20	VDD

4.2 Output Connector :

Location CN2 – 40 pin FPC, PH=0.5mm.

Pin Assign and Definition

Pin No.	Symbol	Pin No.	Symbol	Pin No.	Symbol	Pin No.	Symbol
1	VCOM	11	LVDS1-	21	LVDS3+	31	LED-
2	VDD	12	LVDS1+	22	GND	32	LED-
3	VDD	13	GND	23	NC	33	R/L
4	NC	14	LVDS2-	24	NC	34	U/D
5	RESET	15	LVDS2+	25	GND	35	VGL
6	STBYB	16	GND	26	NC	36	GND
7	GND	17	LVDSCLK-	27	NC	37	GND
8	LVDS0-	18	LVDSCLK+	28	SELB	38	VGH
9	LVDS0+	19	GND	29	AVDD	39	LED+
10	GND	20	LVDS3-	30	GND	40	LED+

5. PCB Dimension

